This listing of claims replaces all prior versions, and listings of claims in the instant application:

Listing of Claims:

1. (Previously Presented) A method for managing remote software code update, comprising:

receiving, by a processor, a message from an embedded device, said embedded device comprising a first code space comprising at least one segment and a second code space comprising one or more segments wherein said processor is included in a device different from said embedded device;

decoding, by said processor, said message to determine an execution mode of said embedded device;

indicating, by said processor, code in said first code space is corrupted if said execution mode indicates said embedded device is executing in said second code space; and

indicating, by said processor, said code in said first code space is valid if said execution mode indicates said embedded device is executing in said first code space.

2. (Previously Presented) The method of claim 1, further comprising:

receiving self-test results from said embedded device if said first code space is valid; and

indicating, by said processor, code in said second code space must be restored when said self-test results indicate said code in said second code space is invalid.

3. (Previously Presented) The method of claim 1, further comprising filtering subsequent communications from said

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processor to said embedded device if said execution mode indicates said embedded device is executing in said second code space.

4. (Currently Amended) An apparatus for managing remote software code update, the apparatus comprising:

means for receiving, by a processor, a message from an embedded device, said embedded device comprising a first code space comprising at least one segment and a second code space comprising one or more segments wherein said processor is included in a device different from said embedded device;

means for decoding, by said processor, said message to determine an execution mode of said embedded device;

means for indicating, by said processor, code in said first code space is corrupted if said execution mode indicates said embedded device is executing in said second code space; and

means for indicating, by said processor, said code in said first code space is valid if said execution mode indicates said embedded device is executing in said first code space.

5. (Previously Presented) The apparatus of claim 4, further comprising:

means for receiving self-test results from said embedded device if said first code space is valid; and

means for indicating, by said processor, code in said second code space must be restored when said self-test results indicate said code in said second code space is invalid.

6. (Previously Presented) The apparatus of claim 4, further comprising means for filtering subsequent

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communications from said processor to said embedded device if said execution mode indicates said embedded device is executing in said second code space.

7. (Previously Presented) An apparatus for managing remote software code update, comprising:

a memory;

a network interface coupled to said memory and configured to receive a message from an embedded device, said embedded device comprising a first code space comprising at least one segment and a second code space comprising one or more segments; and

a processor coupled to said network interface and configured to decode said message to determine an execution mode of said embedded device, said processor further configured to indicate code in said first code space is corrupted if said execution mode indicates said embedded device is executing in said second code space, and said processor further configured to indicate said code in said first code space is valid if said execution mode indicates said embedded device is executing in said first code space wherein said processor is included in a device different from said embedded device.

8. (Original) The apparatus of claim 7 wherein

said network interface is further configured to receive self-test results from said embedded device if said first code space is valid; and

said processor is further configured to indicate code in said second code space must be restored when said self-test results indicate said code in said second code space is invalid.

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9. (Original) The apparatus of claim 7 wherein said processor is further configured to filter subsequent communications to said embedded device if said execution mode indicates said embedded device is executing in said second code space.

10. (Previously Presented) A method for managing remote software code update, comprising:

examining, by a processor, a message received from a remote device to determine an execution mode of said remote device, said remote device comprising a first code space comprising at least one segment and a second code space comprising one or more segments wherein said processor is included in a device different from said remote device; and

indicating validity of code in said first code space based on whether said execution mode indicates said remote device is executing in said second code space.

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